

**Merchant & Gould**

An Intellectual Property Law Firm

3200 IDS Center  
80 South Eighth Street  
Minneapolis, Minnesota  
55402-2215 USA  
TEL 612.332.5300  
FAX 612.332.9081  
www.merchant-  
gould.com

**Fax Transmission** | January 19, 2005

A Professional Corporation

TO: Commissioner for Patents  
Attn: Examiner PATEL  
Patent Examining Corps  
Facsimile Center  
Washington, D.C. 20231

FROM: Douglas P. Mueller

OUR REF: 10873.644US01  
TELEPHONE: 612.371.5237


Total pages, including cover letter: 6PTO FAX NUMBER 1-571-273-1968

If you do NOT receive all of the pages, please telephone us at 612.336.4638, or fax us at 612.332.9081.

Title of OFFICIAL Document Transmitted: Amendment and Response to Election of Species Requirement (originally filed March 19, 2004)

Applicant: KUSUMOTO  
Serial No.: 09/773,502  
Filed: 02/02/01  
Group Art Unit: 2829  
Confirmation No. \_\_\_\_\_  
Our Ref. No.: 10873.644US01

Please charge any additional fees or credit overpayment to Deposit Account No. 13-2725. Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers, if appropriate.

By:   
Name: Douglas P. Mueller  
Reg. No.: 30,300

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below.

  
Sender's Name: Gina Dahl

January 19, 2005  
Date

GEN033.DOT

S/N 09/773,502

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: KUSUMOTO Examiner: PATEL, P.  
Serial No.: 09/773,502 Group Art Unit: 2829  
Filed: February 2, 2001 Docket No.: 10873.644US01  
Title: SEMICONDUCTOR DEVICE HAVING A CONNECTION INSPECTING  
CIRCUIT FOR INSPECTING CONNECTION OF POWER SOURCE  
TERMINALS AND GROUNDING TERMINALS, AND INSPECTION  
METHOD FOR THE SAME

CERTIFICATE UNDER 37 CFR 1.6(d): I hereby certify that this paper is being transmitted by facsimile to the  
U.S. Patent and Trademark Office on January 11, 2005.

By Gina M. Dahl  
Name: Gina M. Dahl

**AMENDMENT AND RESPONSE TO**  
**ELECTION OF SPECIES REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicants elect Species V (Fig. 11) in response to the Election of Species Requirement of March 1, 2004. Claims 68, 70, 73 and 74 read upon the elected species. Please amend the claims as follows:

**Amendments to the Claims** are reflected in the listing of claims that begins on page 2 of this paper.

**Remarks** begin on page 4 of this paper.

S/N 09/773,502

PATENT

**Amendments to the Claims:**

This listing of claims will replace all prior versions and listings of claims in the application.

**Listing of Claims:**

Claims 1-66 are canceled.

67. (Withdrawn) A semiconductor device having a semiconductor integrated circuit, the semiconductor device comprising:

a plurality of leads for supplying the semiconductor device with a first power source from an outside;

a first internal power source line for supplying an internal circuit of the semiconductor integrated circuit with the first power source; and

a first plurality of internal terminals for supplying the first internal power source line with the first power source from the plurality of leads;

wherein all of the first plurality of internal terminals are connected to the first internal power source line via a first switching portion.

68. (Currently amended) ~~The A semiconductor device according to claim 67, further comprising another internal terminal that is connected directly to the first internal power source line not via the first switching portion.~~ having a semiconductor integrated circuit, the semiconductor device comprising:

a plurality of leads for supplying the semiconductor device with a first power source from an outside;

a first internal power source line for supplying an internal circuit of the semiconductor integrated circuit with the first power source; and

a first plurality of internal terminals for supplying the first internal power source line with the first power source from the plurality of leads;

S/N 09/773,502

PATENT

wherein one of the first plurality of internal terminals is connected directly to the first internal power source line, and others thereof are connected to the first internal power source line via a first switching portion.

69. (Withdrawn) The semiconductor device according to claim 67, comprising:
- a second plurality of leads for supplying the semiconductor device with a second power source from the outside;
  - a second internal power source line for supplying the internal circuit with the second power source; and
  - a second plurality of internal terminals for supplying the second internal power source line with the second power source from the second plurality of leads;
- wherein all of the second plurality of internal terminals are connected to the second internal power source line via a second switching portion.

70. (Currently amended) The semiconductor device according to claim ~~69~~ 68, further comprising ~~another internal terminal that is connected directly to the second internal power source line, not via a switching portion.~~

a second plurality of leads for supplying the semiconductor device with a second power source from an outside;

a second internal power source line for supplying the internal circuit of the semiconductor integrated circuit with the second power source; and

a second plurality of internal terminals for supplying the second internal power source line with the second power source from the second plurality of leads;

wherein one of the second plurality of internal terminals is connected directly to the second internal power source line, and others thereof are connected to the second internal power source line via a second switching portion.

S/N 09/773,502

PATENT

71. (Withdrawn) The semiconductor device according to claim 69, further comprising a third switching portion provided between the first and second internal power source lines.
72. (Withdrawn) The semiconductor device according to claim 69, further comprising a current detecting circuit provided between the first and second internal power source lines.
73. (Currently amended) The semiconductor device according to claim ~~67~~ 68, wherein the internal circuit is a group of digital circuits.
74. (Currently amended) The semiconductor device according to claim ~~67~~ 68, wherein the first switching portion comprises a plurality of switching elements, and each of the first plurality of internal terminals is connected to the first internal power source line via a different switching element of the plurality of switching elements.

S/N 09/773,502

PATENT

An early and favorable action on the merits is requested. Please charge any additional fees or credit any overpayment to Deposit Account No. 13-2725.

Respectfully submitted,

MERCHANT & GOULD P.C.  
P.O. Box 2903  
Minneapolis, MN 55402-0903  
(612) 332.5300

Dated: January 19, 2005

By: 

Douglas P. Mueller  
Reg. No. 30,300  
Customer No. 23552

DPM/gnd